Boolean Algebra + Logic gates

Boolean algebra is an algebric structure defined on a set of elements together with two binary operators (+) and (·) :

→ A variable is a symbol for example 'A' used to represent a logical quantity, whose value can be '0' or '1'.

→ The complement of a variable is the inverse of variable and is represented by an over bar, for example '$\bar{A}$'.

→ A literal is a variable or the complement of variable.

- Closure: For any x and y in the alphabet A, x+y and x.y are also in A.

- Boolean value: The value of Boolean variable can be either '1' or '0'.

- Boolean Operators:

  There are '4' boolean Operators –

  (i) AND (·) Operator (A.B)
  (ii) OR (+) Operator (A+B)
  (iii) NOT ($\bar{A}$/A') Operator.
  (iv) XOR (⊕) operator ($A \oplus B = \bar{A}B + B\bar{A}$)

- Operator precedence:

  The operator for evaluating Boolean expression is –

  (i) parenthesis (ii) NOT (iii) AND (iv) OR.

- Duality: If an expression contains only the operations AND, OR and NOT. Then the dual of that expression is by replacing each AND by OR, each OR by AND, all occurrences of '1' by '0' and all occurrences of '0' by 1. principle of duality is useful in determining the complement of a function.

logic expression: $(x.y'.z)+(x.y.z')+(y.z)+0$

Duality of above logic expression is:
$(x+y'+z).(x+y+z').(y+z).1$.

- Boolean function:

→ Any Boolean functions can be from binary variables and the boolean operators •, + and ' (for AND, OR and NOT respectively.

→ for a given value of variable, the function can take only one value either '0' or '1'.

→ A Boolean function can be shown by a ~~that~~ truth table. The shown a function in a truth ~~at~~ table we need a list of the 2^n combinations of 0's and 1's of the 'n' binary variables and a column showing the combination for which the function is equal to 1 or 0. So, the table will have 2^n rows and column for each input variable and the final output.

- A function can be specified or represented —

(1) A truth table.
(2) A circuit.
(3) A Boolean expression.
(4) SOP (sum of products).
(5) POS (product of Sum).
(6) Canonical SOP.
(7) canonical POS.

- Important Boolean operations over boolean values —

AND Operation	OR Operation
$0 \cdot 0 = 0$	$1 + 1 = 1$
$1 \cdot 1 = 1$	$0 + 0 = 0$
$0 \cdot 1 = 1 \cdot 0 = 0$	$1 + 0 = 0 + 1 = 1$
$0' = 1$	$1' = 0$

- Table of some basic theorems —

law/theorem	law of addition	law of multiplication
Identity law	$X + 0 = X$	$X \cdot 1 = X$
complement law	$X + \bar{X} = 1$	$X \cdot \bar{X} = 0$
Idempotent law	$X + X = X$	$X \cdot X = X$
Dominant law	$X + 1 = 1$	$X \cdot 0 = 0$
Involution law	$(X')' = X$	
Commutative law	$X + Y = Y + X$	$X \cdot Y = Y \cdot X$

Associative law	$x+(y+z)=(x+y)+z$	$x\cdot(y\cdot z)=(x\cdot y)\cdot z$
Distributive law	$x+yz=(x+y)(x+z)$	$A(B+C)=AB+AC$
Demorgan's law	$(x+y)'=x'\cdot y'$	$(x\cdot y)'=x'+y'$.
Absorption law	$x+xy=x$	$x(x+y)=x$.

- Important theorem used in simplification =

→ NOT - Operation theorem : $\overline{\overline{A}}=A$.

→ AND - operation theorem:

$$A\cdot A=A$$
$$A\cdot 1=A$$
$$A\cdot 0=0$$
$$A\cdot\overline{A}=0$$

→ OR - operation theorem:

$$A+A=A$$
$$A+1=1$$
$$A+0=A$$
$$A+\overline{A}=1$$

- Distribution theorem =

$$\boxed{A+BC=(A+B)(A+C)}$$

Note-

$$(A+\overline{A}B)\rightarrow(A+B)$$
$$(A+\overline{A}\,\overline{B})\longrightarrow(A+\overline{B})$$
$$(\overline{A}+AB)\longrightarrow(\overline{A}+B)$$
$$(\overline{A}+A\overline{B})\longrightarrow(\overline{A}+\overline{B})$$

- Demorgan's theorem :

$$\overline{(A+B+C)} = \bar{A}\cdot\bar{B}\cdot\bar{C}$$

$$\overline{(A\cdot B\cdot C)} = \bar{A}+\bar{B}+\bar{C}$$

- Transposition theorem :

$$(A+B)(A+C) = A+BC$$

- Consensus Theorem : This theorum is used to eliminate redundant term. It is applicable only when if a boolean function contain three variable. Each variable are used to times. only one variable is complemented or uncomplemented. Then the related terms so that complemented or uncomplemented variable is the answer -

Ex -

(I) $AB+\bar{B}C+AC = AB+\bar{B}C$.

(II) $\bar{A}B+\bar{B}C+\bar{A}C = \bar{A}B+\bar{B}C$.

(III) $AB+\bar{A}C+BC = AB+\bar{A}C$.

(IV) $A\bar{B}+AC+BC = A\bar{B}+BC$.

- problems on Boolean algebra —

- Simplify the following Boolean expressions to a minimum number of literals —

LEVEL-1

(a) $xy + xy'$

$\Rightarrow x(y+y')$

$\Rightarrow x \cdot 1 \Rightarrow 1$

(b) $(x+y)(x+\bar{y})$

$\Rightarrow x + y\bar{y}$ (transposition theorem)

$\Rightarrow x+0 \Rightarrow x.$

(c) $xyz + \bar{x}y + xy\bar{z}$

$\Rightarrow xy(z+\bar{z}) + \bar{x}y$

$\Rightarrow xy + \bar{x}y$

$\Rightarrow y(x+\bar{x})$

$\Rightarrow y \cdot 1 \Rightarrow y$

(d) $(\overline{A+B})(\overline{\bar{A}+\bar{B}})$

$\Rightarrow$ ~~(A+B)~~ $(\bar{A}\cdot B)(A \cdot B)$

$\Rightarrow 0 \cdot 0 \Rightarrow 0.$

LEVEL-2

(1) $\bar{A}\bar{C} + ABC + A\bar{C} + A\bar{B}$ (reduce to two literals)

$\Rightarrow \bar{C}(\bar{A}+A) + A(\bar{B}+BC)$

$\Rightarrow \bar{C} + A((\bar{B}+B)(\bar{B}+C))$

$\Rightarrow \bar{C} + A(\bar{B}+C)$

$\Rightarrow \bar{C} + A\bar{B} + AC$

$\Rightarrow (\bar{C}+A)(\bar{C}+C) + A\bar{B}$

$\Rightarrow (\bar{C}+A) + A\bar{B}$

$\Rightarrow A(1+\bar{B}) + \bar{C}$

$\Rightarrow A + \bar{C}.$

(2) $\bar{A}B(\bar{D}+CD)+B(\bar{A}+\bar{A}CD)$ → reduce to one literals.

→ $\bar{A}B(\bar{D}+C)+B(A+CD)$

$\Rightarrow B(\bar{A}\bar{D}+\bar{A}C+A+CD)$

$\Rightarrow B(\bar{A}\bar{D}+A+C+CD)$

$\Rightarrow B(A+\bar{D}+C+CD)$

$\Rightarrow B(A+C+\bar{D}+C)$ $\quad \boxed{C+C=C}$

$\Rightarrow B(A+C+\bar{D})$

$\Rightarrow B(\bar{\bar{A}}+\bar{\bar{C}}+\bar{D})$ (Demorgan's law)

$\Rightarrow B\,\overline{(\bar{A}.\bar{C}.D)}$

[illegible] $\quad \bar{a}\cdot\bar{b}=\overline{(a+b)}$

[illegible]

- Find the complement —

(a) $F=x\bar{y}+\bar{x}y$

→ $F_{dual}=(x+\bar{y})(\bar{x}+y)$

$F_C=(\bar{x}+y)\cdot(x+\bar{y})$

OR

$\bar{F}=\overline{(x\bar{y}+\bar{x}y)}$

$\Rightarrow \overline{x\bar{y}}\cdot\overline{\bar{x}y}$

$\Rightarrow (\bar{x}+y)(x+\bar{y})$

(b) $F=(\bar{A}B+CD)\bar{E}+E$

→ $F_{dual}=\{(\bar{A}+B)\cdot(C+D)+\bar{E}\}\cdot E$ $\quad \boxed{E\cdot\bar{E}=0}$

$F_{com}=\{(A+\bar{B})\cdot(\bar{C}+\bar{D})+E\}\bar{E}$

$=(A+\bar{B})(\bar{C}+\bar{D})\bar{E}$

ⓒ $(\bar{x}+y+\bar{z})\ (x+\bar{y})\ (x+z)$

$\Rightarrow$

$f_d = \bar{x}\cdot y\bar{z} + x\bar{y} + xz$

$F_{com} = x\bar{y}z + \bar{x}y + \bar{x}\bar{z}$

- List the truth table of the function ⇒

ⓐ $F = xy + \bar{x}y + \bar{y}z$

$= y(x+\bar{x}) + \bar{y}z$

$= y + \bar{y}z$ note $[A+BC = (A+B)(A+C)]$

$F = y + z$

table -

x	y	z	F = y+z
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

ⓑ $F = \bar{x}\bar{z} + yz$.

$\Rightarrow$

x	y	z	$f = \bar{x}\bar{z} + yz$
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

$1\cdot 1 + 0\cdot 0 = 1$

$1\cdot 0 + 0\cdot 1 = 0$

$1\cdot 0 + 1$

(Q)- Reduce the expression, $f = \overline{(A+\bar{B}C)}\,(A\bar{B}+ABC)$

→ $f = \overline{(A+\bar{B}C)}\,(A\bar{B}+ABC)$

$\Rightarrow (\bar{A}\cdot\overline{\bar{B}C})\,(A(\bar{B}+BC))$

$\Rightarrow (\bar{A}\,(B+\bar{C}))\,(A\,(\bar{B}+C))$

$\Rightarrow (\bar{A}B+\bar{A}\bar{C})\,(A\bar{B}+AC)$

$\Rightarrow 0+0+0+0 \Rightarrow 0.$

(Q) Reduce the function to one literal.

$F=(B+BC)\,(B+\bar{B}C)\,(B+D)$

→ $f=(B+BC)\,(B+\bar{B}C)\,(B+D)$

$\Rightarrow B(1+C)\,((B+\bar{B})(B+C))\,(B+D)$

$\Rightarrow B(1+C)\,(B+C)\,(B+D)$

$\Rightarrow B\,(B+C)\,(B+D)$

$\Rightarrow B\,(B+BD+BC+CD)$

$\Rightarrow B\,(B+CD)$

$\Rightarrow B+BCD$

$\Rightarrow B(1+CD)$

$\Rightarrow B.$

(Q) Reduce the Boolean expression -

$F= AB+A(B+C)+\bar{B}(B+D)$

$\Rightarrow AB+AB+AC+\underbrace{\bar{B}B}_{0}+\bar{B}D$

$\Rightarrow AB+AC+B\bar{D}$

Ⓠ Reduce the boolean expression -

$F = \bar{A}B + \bar{A}B\bar{C} + \bar{A}BCD + \bar{A}B\bar{C}\bar{D}E.$

$\Rightarrow \bar{A}B(1 + \bar{C} + CD + \bar{C}\bar{D}E)$

$\Rightarrow \bar{A}B \cdot 1$

$\Rightarrow \bar{A}B.$

Ⓠ Reduce the boolean expression -

$F = AB + \overline{AC} + A\bar{B}C(AB + C)$

$\Rightarrow AB + \bar{A} + \bar{C} + 0 + A\bar{B}C$

$\Rightarrow \bar{A} + B + \bar{C} + A\bar{B}C$

$\Rightarrow \bar{A} + B + \bar{C} + A\bar{B}$

$\Rightarrow \bar{A} + B + \bar{C} + A$

$\Rightarrow 1 + B + \bar{C}$

$\Rightarrow 1.$

Ⓠ Reduce the given boolean ex -

$F = \overline{\overline{AB} + \bar{A} + AB}$

$\Rightarrow (\overline{\overline{AB}}) \cdot (\overline{\bar{A} + AB})$ [$A + BC = (A+B)(A+C)$

$\Rightarrow (AB)\,\overline{(\bar{A} + B)}$

$\Rightarrow (AB)\,(A\bar{B})$ | $\overline{\bar{A} + B} = A.\bar{B}$

$= 0.$

(Q) If $x=1$ in the logic function –

$$\left[x + z\{\bar{y} + (\bar{z} + x\bar{y})\}\right]\{\bar{x} + \bar{z}(x+y)\} = 1$$

(a) $y = z$ (b) $y = \bar{z}$ (c) $z = 1$ ✓(d) $z = 0$.

→ $\left[1 + z\{\bar{y} + (\bar{z} + x\bar{y})\}\right]\{0 + \bar{z}(1+y)\} = 1$

⇒ $1 \cdot \bar{z} = 1$

⇒ $\bar{z} = 1$

$z = 0$

(Q) The boolean expression –

$$Y = \bar{A}\bar{B}\bar{C}D + \bar{A}BC\bar{D} + A\bar{B}\bar{C}D + AB\bar{C}\bar{D}$$

reduce to

(a) $Y = \bar{A}\bar{B}\bar{C}D \quad \bar{A}B\bar{C} \quad A\bar{C}D$

(b) $Y = \bar{A}\bar{B}\bar{C}D \quad BC\bar{D} \quad A\bar{B}\bar{C}D$

(c) $Y = \bar{A}BC\bar{D} \quad \bar{B}\bar{C}D \quad A\bar{B}\bar{C}D$

✓(d) $Y = \bar{A}BC\bar{D} \quad \bar{B}\bar{C}D \quad AB\bar{C}\bar{D}$

→ Solu $Y = \bar{A}\bar{B}\bar{C}D + \bar{A}BC\bar{D} + A\bar{B}\bar{C}D + AB\bar{C}\bar{D}$

$= \bar{B}\bar{C}D(A+\bar{A}) + \bar{A}BC\bar{D} + AB\bar{C}\bar{D}$

$= \bar{B}\bar{C}D + \bar{A}BC\bar{D} + AB\bar{C}\bar{D}$.

(Q) $W = R + \bar{P}Q + \bar{R}S$.

$X = PQ\bar{R}\bar{S} + \bar{P}\bar{Q}\bar{R}\bar{S} + P\bar{Q}\bar{R}\bar{S}$

$Y = \overline{RS + PR + \bar{P}\bar{Q} + \bar{P}\bar{Q}}$

$Z = \overline{R + S + PQ + \bar{P}\bar{Q}\bar{R} + P\bar{Q}\bar{S}}$

then

✓(a) $W = Z,\ X = \bar{Z}$ (b) $W = Z,\ X = Y$

(c) $W = Y$ (d) $W = Y = \bar{Z}$

→

$W = R + \bar{P}Q + \bar{R}S$

$= (R+\bar{R})(R+S) + \bar{P}Q$ $\quad$ $\boxed{A+BC = (A+B)(A+C)}$

$= R + S + \bar{P}Q$ —— (I)

$X = PQ\bar{R}\bar{S} + \bar{P}\bar{Q}\bar{R}\bar{S} + P\bar{Q}\bar{R}\bar{S}$

$= PQ\bar{R}\bar{S} + \bar{Q}\bar{R}\bar{S}(\bar{P}+P)$

$= \bar{R}\bar{S}(PQ + \bar{Q})$

$= \bar{R}\bar{S}(P + \bar{Q})$

$= \bar{R}\bar{S}P + \bar{R}\bar{S}\bar{Q}$ —— (II)

$Y = RS + \overline{PR + \bar{P}\bar{Q} + \bar{P}Q}$

$= RS + \overline{PR + \bar{P}Q}$

$= RS + \overline{PR}\;\overline{\bar{P}Q}$

$= RS + (\bar{P}+\bar{R})(P+\bar{Q})$

$=$ ~~RQ~~ $RS + \bar{P}Q + P\bar{R} + \bar{R}Q$ —— (III)

$Z = R + S + \overline{PQ + \bar{P}\bar{Q}\bar{R} + P\bar{Q}\bar{S}}$

$= R+S + \overline{P(Q+\bar{Q}\bar{S}) + \bar{P}\bar{Q}\bar{R}}$

$= R+S + \overline{P(Q+\bar{S}) + \bar{P}\bar{Q}\bar{R}}$

$= R+S + \overline{PQ + P\bar{S} + \bar{P}\bar{Q}\bar{R}}$

$= R+S + \overline{PQ}\cdot\overline{P\bar{S}}\cdot\overline{\bar{P}\bar{Q}\bar{R}}$

$= R+S + (\bar{P}+\bar{Q})(\bar{P}+S)(P+Q+R)$

~~= R+S + (P̄+Q̄)(P̄Q + RP̄ + SP̄ + SQ̄ + SR)~~

~~[illegible]~~

$= R+S + (\bar{P} + \bar{P}S + \bar{Q}\bar{P} + \bar{Q}S)(P+Q+R)$

$= R+S + (\bar{P} + \bar{Q}\bar{P} + \bar{Q}S)(P+Q+R)$

$= R + S + \bar{P}Q + \bar{P}R + \bar{Q}\bar{P}R + P\bar{Q}S + \bar{Q}SR$

$= R + S + \bar{P}Q + \bar{P}R + P\bar{Q}S + \bar{Q}RS$

$= R + \bar{P}R + \bar{Q}RS + S + P\bar{Q}S + \bar{P}Q$

$= R[1 + \bar{P} + \bar{Q}S] + S[1 + P\bar{Q}] + \bar{P}Q.$

$Z = R + S + \bar{P}Q. \quad \text{—— (IV)}$

here, equ (I) and (IV) same equation.

$\boxed{W = Z},$

$Z_{dual} = RS(\bar{P} + Q)$

$X = \bar{R}\bar{S}(P + \bar{Q}).$

$Z = R + S + \bar{P}Q.$

$Z_C = \bar{R}\bar{S}(P + \bar{Q})$

$\bar{Z} = \overline{R + S + \bar{P}Q}$

$= \bar{R}\bar{S}(P + \bar{Q})$

$\boxed{X = \bar{Z}}$

- **Logic gates** — A logic gate is an idealised or physical device implementing a Boolean function, that is, it performs a logical operation in one or more logic i/p and produce a single logic o/p.

Logic gates can be classified as —

(I) NOT, AND, OR are basic gates.
(II) NAND, NOR are universal gates.
(III) EXOR, EXNOR are arithmetic circuit or code convertor or comparators.

- Not gate (Invertor):

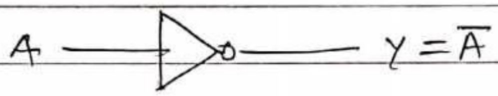


table -

i/p A	o/p $Y=\overline{A}$
0	1
1	0

- AND gate —

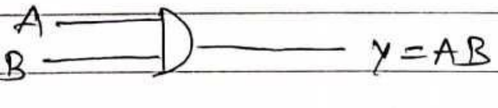


table -

A	B	Y
0	0	0
0	1	0
1	0	0
1	1	1

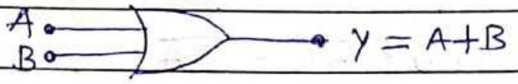
Properties of AND gate -

→ follow Commutative and Associative law.

$AB = BA$

$ABC = A(BC) = (AC)B.$

• OR Gate -



(OR gate symbol)

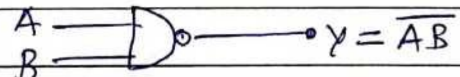
Truth table of OR Gate:

Inputs		Output
A	B	$Y = A+B$
0	0	0
0	1	1
1	0	1
1	1	1

properties of OR logic -

(I) commutative law $= (A+B) = (B+A)$.
(II) Associative law $= A+B+C = (A+B)+C = B+(A+C)$.

• NAND Gate:



(NAND gate symbol)

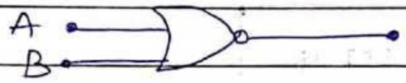
Truth table of NAND gate -

Input		Output
A	B	$Y = \overline{AB}$
0	0	1
0	1	1
1	0	1
1	1	0

properties of NAND gate -

(1) commutative law : $\overline{AB} = \overline{BA}$.
(2) Associative law : $\overline{\overline{AB}C} \neq \overline{A\overline{BC}}$.

• NOR gate -



$Y = \overline{A+B}$

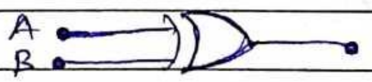
(NOR gate Symbol)

Truth table of NOR gate -

Input		output
A	B	$Y = \overline{A+B}$
0	0	1
0	1	0
1	0	0
1	1	0

→ NOR Gate follows commutative law but not follow associative law.

• EXOR Gate :



$Y = A \oplus B$
$= \overline{A}B + A\overline{B}$

(Ex-OR gate symbol)

Truth table of Ex-OR Gate -

Input		output
A	B	$Y = A \oplus B = \bar{A}B + A\bar{B}$
0	0	0
0	1	1
1	0	1
1	1	0

properties of EX-OR Gate –

- Enable Input = 0
- Disable input = 1
- It is called stair case switch.
- When both input are different, then output become high or logic '1'.
- When both Input are same, then output become low or logic '0'.
- Arithmetic gate. • Inequality detector.

Note: $A \oplus A = 0$

$A \oplus \bar{A} = 1$

$A \oplus 0 = A$

$A \oplus 1 = \bar{A}$.

→ follow Associative and commutative both law.

- EX-NOR Gate:

$Y = A \odot B = AB + \bar{A}\bar{B}$

(EX-NOR gate symbol).

Truth table of EX-NOR Gate –

Input A	Input B	Output $Y = A \odot B = AB + \bar{A}\bar{B}$
0	0	1
0	1	0
1	0	0
1	1	1

properties of EXNOR Gate –

- Enable Input = 1
- Disable Input = 0
- when both the input are same, then output become high or logic '1'.
- when both the Input are diff, then output become low or logic '0'.
- equality detector.

- NAND and NOR Gate universal gate –

logic gate	Required no. of NAND Gate	Required no. of NOR Gate
NOT	1	1
AND	2	3
OR	3	2
EXOR	4	5
EXNOR	5	4

• Implementation of NAND Gate –

procedure to Implement Boolean functions –

→ **Step-1:** Take complement of given function & apply DeMorgan's theorem.

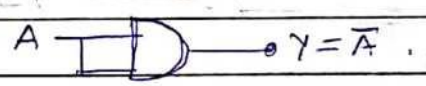
→ **Step-2:** Take one more time complement to get original function. And implement using NAND gates.

• NAND Gate as universal gate –

(i) NOT gate:

$$Y = \overline{A}$$

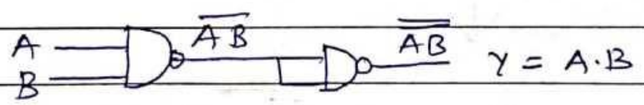
$$\text{NAND}, \; Y = \overline{A \cdot B} = \overline{A \cdot A} = \overline{A}$$



(ii) AND gate:

$$Y = A \cdot B$$

$$\text{NAND}, \; Y = \overline{A \cdot B}$$

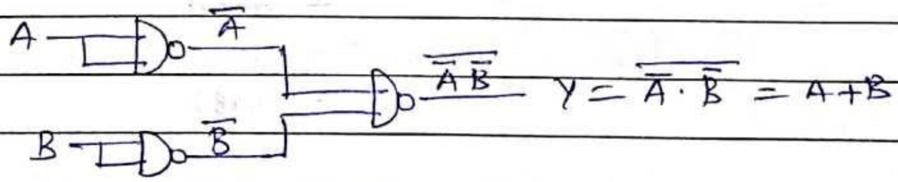


(iii) OR gate:

$$Y = A + B$$

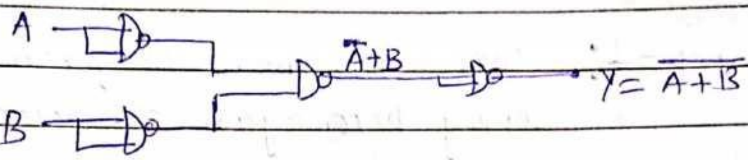
$$\text{Step-I}, \; \overline{Y} = \overline{A+B} = \overline{A} \cdot \overline{B}$$

$$\text{Step-II} \; \overline{\overline{Y}} = \overline{\overline{A} \cdot \overline{B}}$$



(iv) NOR gate:

$$Y = \overline{A+B}$$



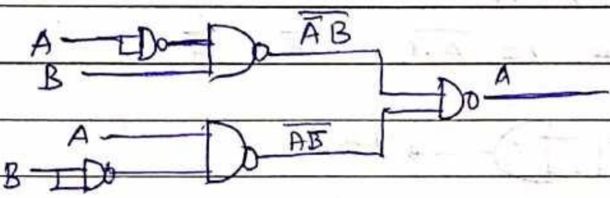
(v) EX-OR gate:

$$Y = A \oplus B = \bar{A}B + A\bar{B}$$

S-1: $\bar{Y} = \overline{\bar{A}B + A\bar{B}}$

$= \overline{\bar{A}B} \; \overline{A\bar{B}}$

S-2: $\bar{\bar{Y}} = \overline{\overline{\bar{A}B} . \overline{A\bar{B}}}$

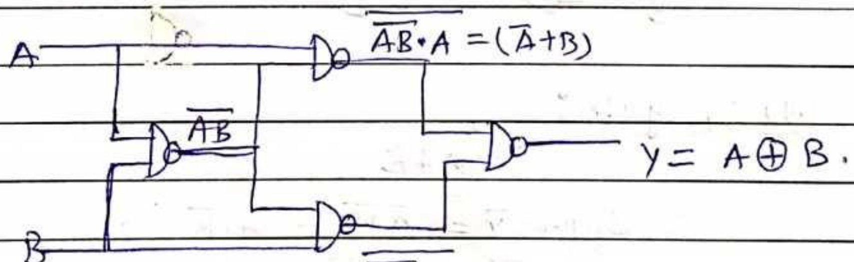


$y = \overline{\overline{\bar{A}B} . \overline{A\bar{B}}}$

$= \overline{(A+\bar{B}) \; (\bar{A}+B)}$ ✓✓

$= \overline{(A+\bar{B})} + \overline{(\bar{A}+B)}$

$= \bar{A}B + A\bar{B}$.



$\overline{\overline{AB} . B}$

$= \overline{(\bar{A}+\bar{B})B}$

$= \overline{\bar{A}B}$

$= (A+\bar{B})$

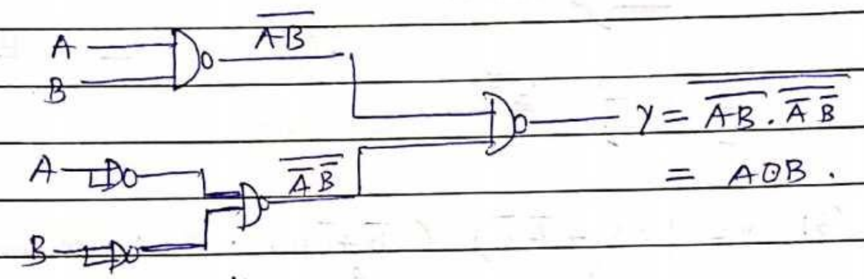
(vi) Ex-NOR gate :

$$Y = A \odot B$$
$$= AB + \bar{A}\bar{B}$$

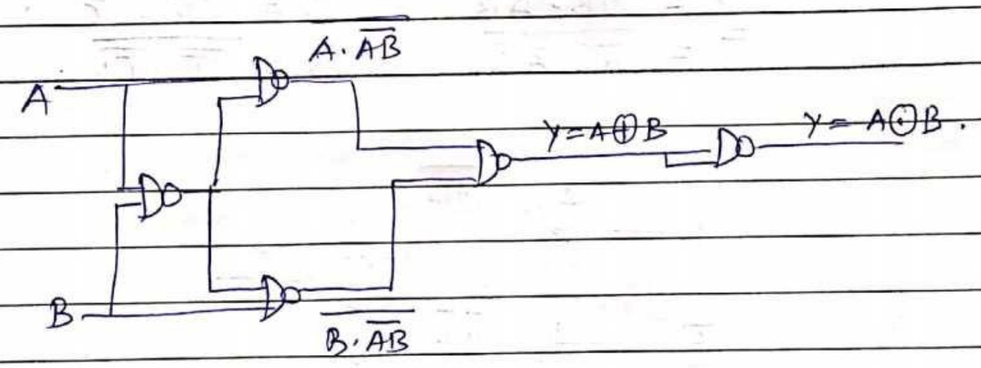
Step-1 : $\bar{Y} = \overline{AB + \bar{A}\bar{B}}$

$\bar{Y} = \overline{AB} \cdot \overline{\bar{A}\bar{B}}$

Step-II : $\bar{\bar{Y}} = \overline{\overline{AB} \cdot \overline{\bar{A}\bar{B}}}$.



⇓



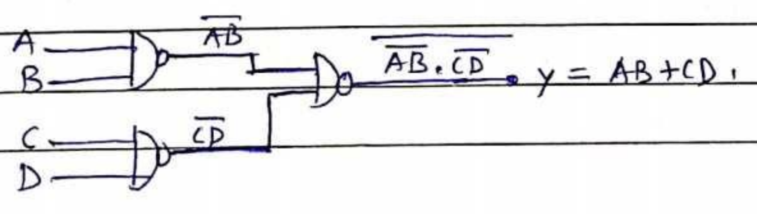
5 NAND gate required to implement one EX-NOR gate.

- Implementation of Boolean functions using only NAND gates—

(i) $Y = AB + CD$

Step-1 : $\bar{Y} = \overline{AB + CD}$
$= \overline{AB} \cdot \overline{CD}$

Step-2 : $\bar{\bar{Y}} = \overline{\overline{AB} \cdot \overline{CD}}$.



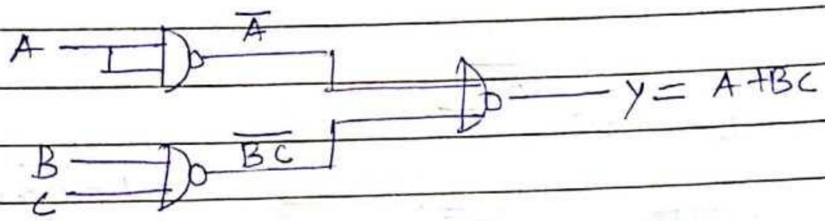
(2) $y = A + BC$

→ ~~[illegible]~~

S-1: $\bar{y} = \overline{A+BC}$

$= \bar{A} \cdot \overline{BC}$

S-2: $\bar{\bar{y}} = \overline{\bar{A} \cdot \overline{BC}}$



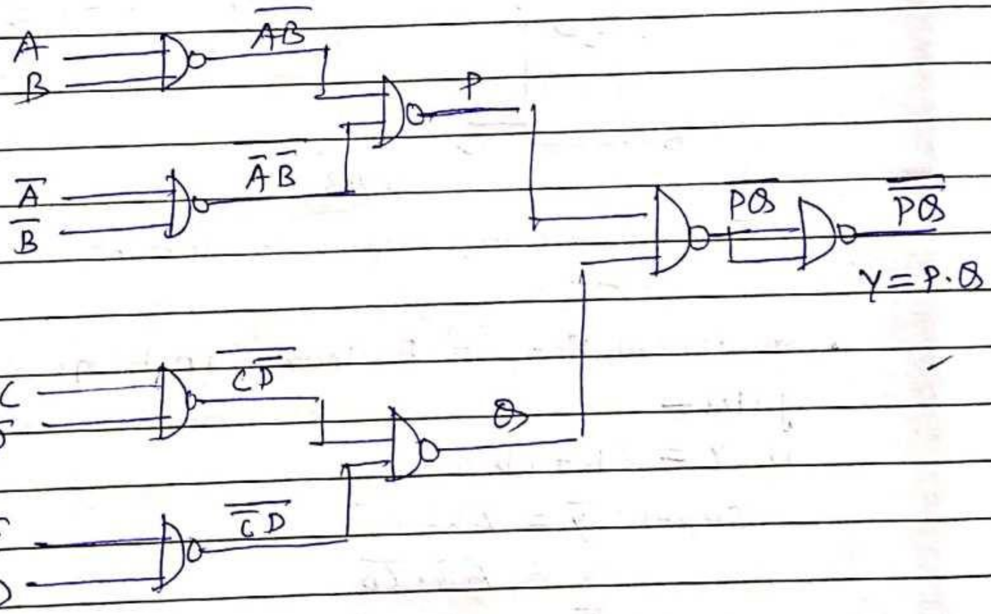
(3) $y = \underbrace{(AB + \bar{A}\bar{B})}_{P}\ \underbrace{(C\bar{D} + \bar{C}D)}_{Q}$. (complement are available)

→

$P = AB + \bar{A}\bar{B}$ $\qquad Q = C\bar{D} + \bar{C}D$

$\bar{P} = \overline{AB} \cdot \overline{\bar{A}\bar{B}}$ $\qquad \bar{Q} = \overline{C\bar{D}} \cdot \overline{\bar{C}D}$

$\bar{\bar{P}} = \overline{\overline{AB}\ \overline{\bar{A}\bar{B}}}$ $\qquad \bar{\bar{Q}} = \overline{\overline{C\bar{D}} \cdot \overline{\bar{C}D}}$



(4) $Y = (A+B)(C+D)$

(i) using only two I/p NAND gates.

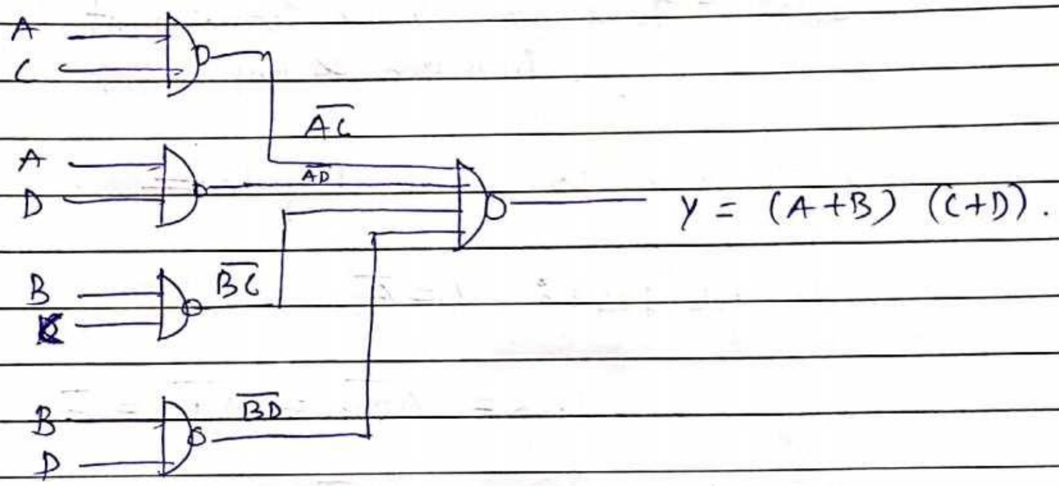
(ii) using only NAND gates.

→

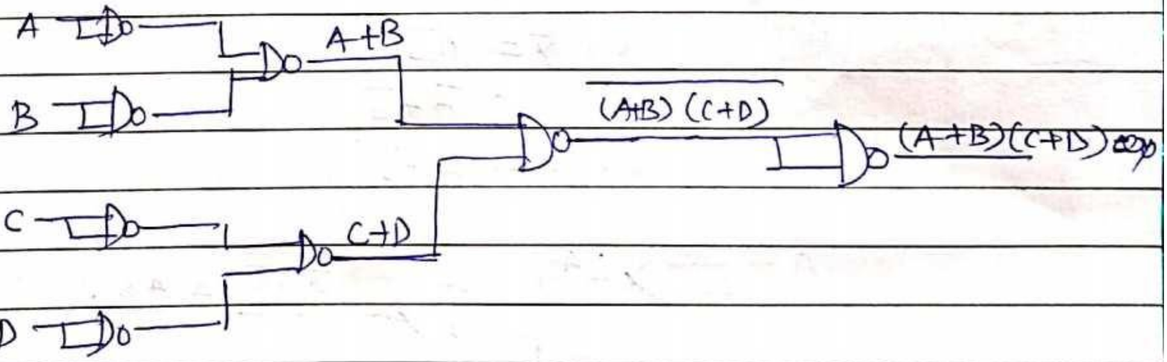
(ii) $Y = AC + AD + BC + BD$

S-1: $\overline{Y} = \overline{AC}\ \overline{AD}\ \overline{BC}\ \overline{BD}$

S-2: $\overline{\overline{Y}} = \overline{\overline{AC}\ \overline{AD}\ \overline{BC}\ \overline{BD}}$



(i) $y = (A+B)\ (C+D)$



(8 NAND gate required).

• Implementation of NOR Gate :

$$Y = \overline{A+B}$$

Procedue :-

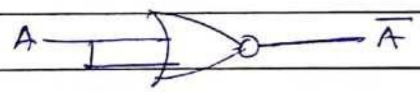
Step1:- Apply complement for the given logic function and apply De-morgan's Theorem. (if it is not in the required form)

Step2 :- Take one more Complement to get original logic function & implement using NOR gates.

• NOR Gate as universal gate =

(i) NOT gate : $Y = \overline{A}$

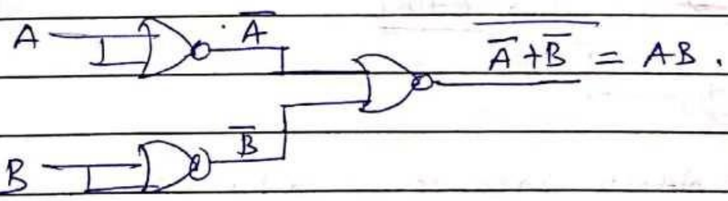
$$NOR = \overline{A+B} = \overline{A+A} = \overline{A}$$



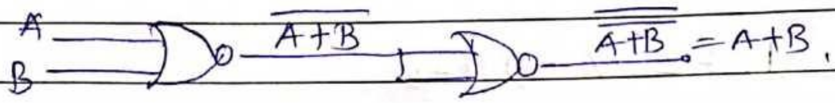
(ii) AND gate : $Y = A \cdot B$

(i) $\overline{Y} = \overline{AB}$
$= \overline{A} + \overline{B}$

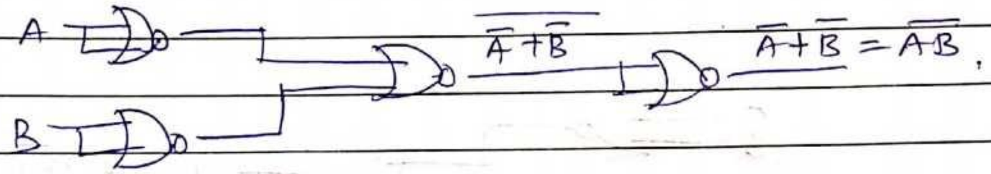
(ii) $\overline{\overline{Y}} = \overline{\overline{A} + \overline{B}}$



(iii) <u>OR gate</u>: $Y = A + B$



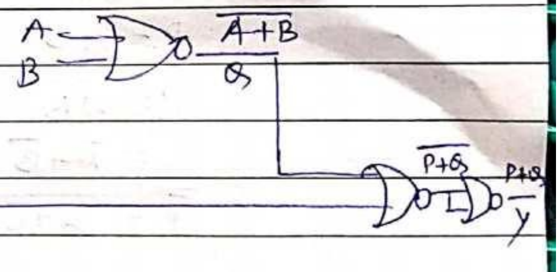
(iv) <u>NAND gate</u>: $Y = \overline{AB}$
$= \overline{A} + \overline{B}$

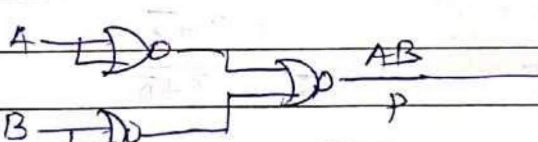


(V) <u>EX-NOR</u>: $Y = A \odot B = \underset{P}{\underline{AB}} + \underset{Q}{\underline{\overline{A}\,\overline{B}}}$

$P = AB$
(i) $\overline{P} = \overline{AB}$
$= \overline{A} + \overline{B}$
(ii) $\overline{\overline{P}} = \overline{\overline{A} + \overline{B}}$

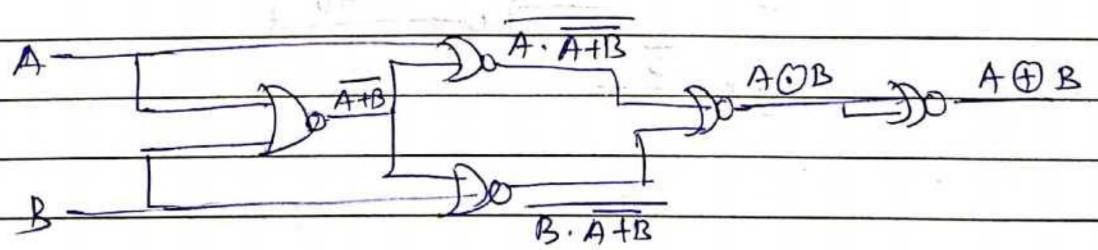
$Q = \overline{A}\,\overline{B}$
$= \overline{A+B}$





minimum no. of NOR gate require EX-NOR gate is '4'.

(VI) <u>EX-OR gate</u>: $Y = A \oplus B$
$= \overline{A}B + A\overline{B}$



'5' min no. of NOR required to implement EX-OR gate.

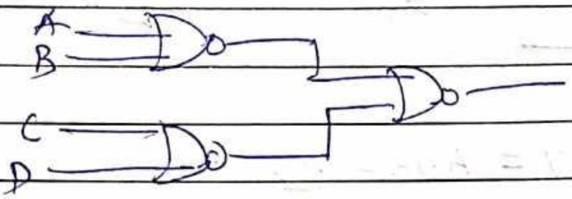
- Implementation of Boolean function using only NOR gates –

(1) $F = (A+B)(C+D)$

→ (i) $\overline{F} = \overline{(A+B)(C+D)}$

$= \overline{(A+B)} + \overline{(C+D)}$

(ii) $\overline{\overline{F}} = \overline{\overline{(A+B)} + \overline{(C+D)}}$



$Y = \overline{\overline{(A+B)} + \overline{(C+D)}}$

$= (A+B)(C+D).$

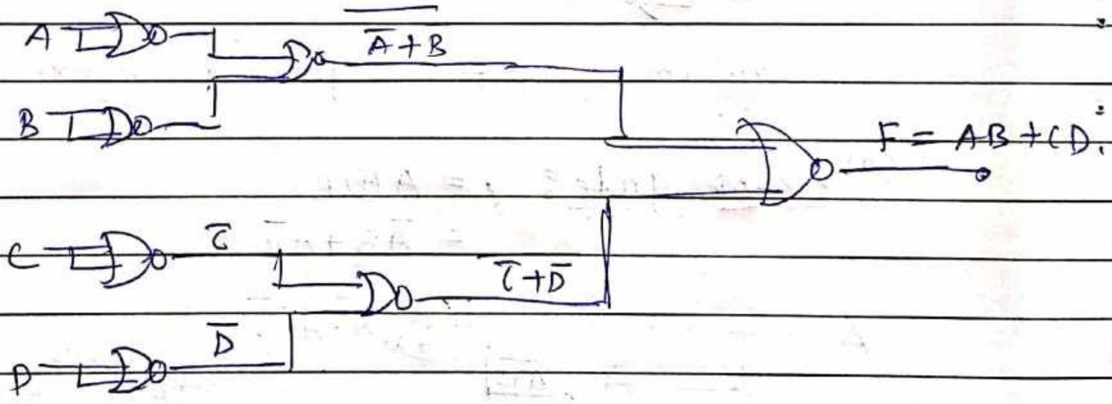
(2) $F = \underbrace{AB}_{P} + \underbrace{CD}_{Q}$.

$P = AB$ | $Q = CD$

$\overline{P} = \overline{AB}$ | $\overline{Q} = \overline{CD}$

$= \overline{A} + \overline{B}$ | $= \overline{C} + \overline{D}$

$\overline{\overline{P}} = \overline{\overline{A} + \overline{B}}$ | $\overline{\overline{Q}} = \overline{\overline{C} + \overline{D}}$



③ $F = a(b+cd) + b\bar{c}$

($x = a(b+cd)$, $y = b\bar{c}$)

$b\bar{c}$

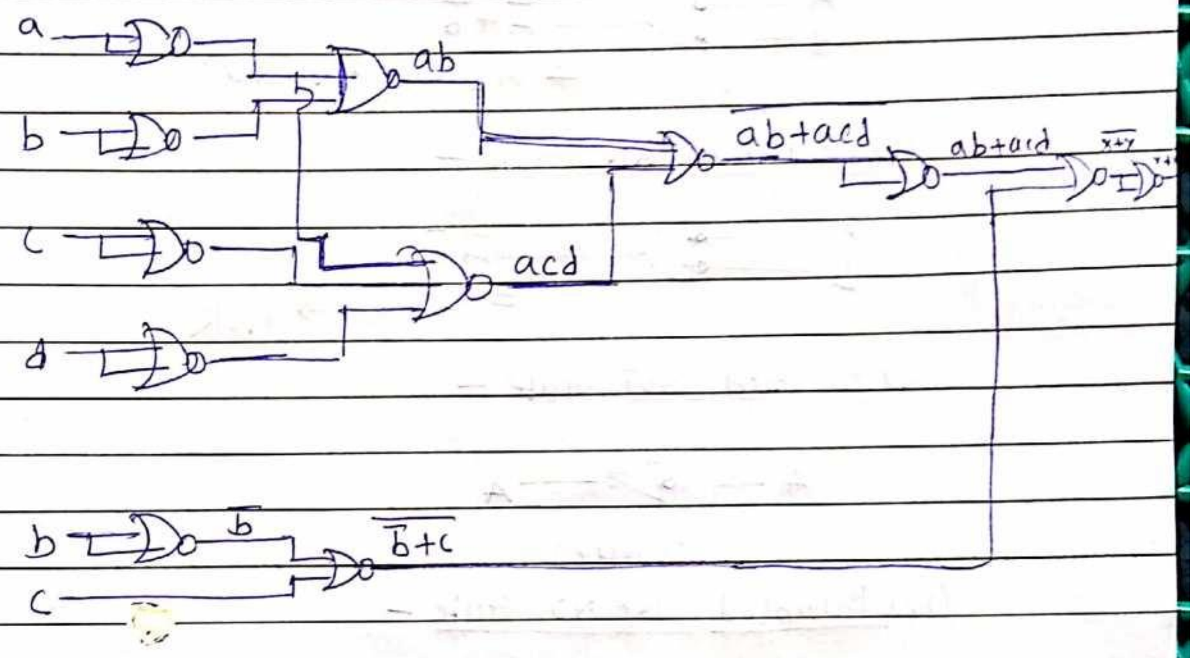
$\bar{b}+c$

$\overline{\bar{b}+c}$

→

$x = a(b+cd)$

$= ab + acd$

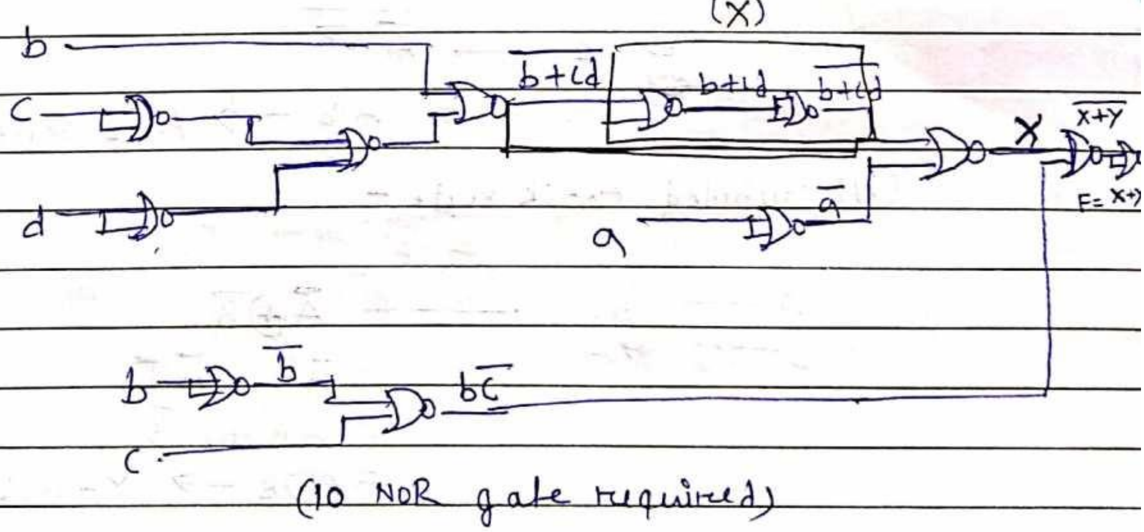


④ Implement the logic function using only two input NOR gates —

$F = a(b+cd) + b\bar{c}$

($x = a(b+cd)$, $y = b\bar{c}$)

$x = a(b+cd)$

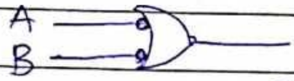


(10 NOR gate required)

- **Alternate logic gate (Bubbled gate):**

"Bubble indicates Not operation".

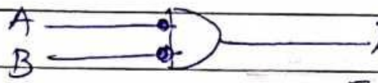
(i) Bubbled OR Gate –



$\bar{A}+\bar{B}$

$= \overline{AB} \rightarrow$ NAND

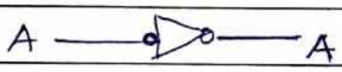
(ii) Bubbled AND Gate –



$\bar{A}\bar{B}$

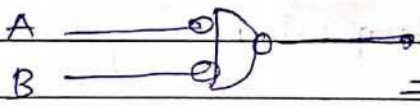
$= \overline{(A+B)} \rightarrow$ NOR.

(iii) Bubbled NOT Gate –



(Buffer)

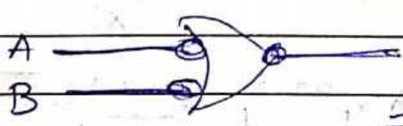
(iv) Bubbled NAND Gate –



$\overline{\bar{A}\bar{B}}$

$= \overline{\overline{(A+B)}} = A+B \Rightarrow$ OR Gate.

(v) Bubbled NOR Gate –



$\overline{\bar{A}+\bar{B}}$

$= A\cdot B \rightarrow$ AND Gate.

(vi) Bubbled ex-OR Gate –

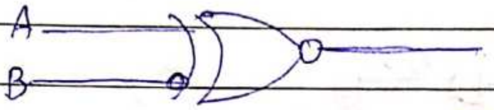
$\bar{A} \oplus B$

$= \bar{\bar{A}}B + \bar{A}\bar{B}$

$= AB + \bar{A}\bar{B}$

$= A \odot B \rightarrow$ X-NOR gate.

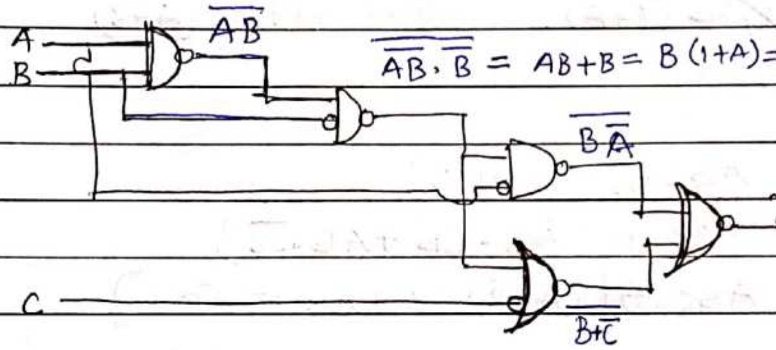
(VII) Bubbled X-NOR gate —



$A \odot \overline{B}$
$= \overline{\overline{A}}\,\overline{B} + \overline{A}\,\overline{\overline{B}}$
$= A\overline{B} + \overline{A}B \rightarrow$ X-OR gate.

gate - 2000

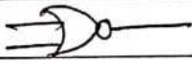
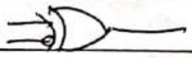
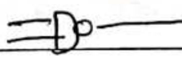
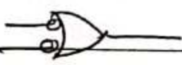
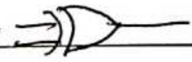
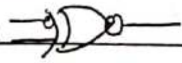
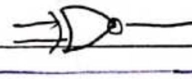
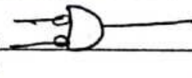
Q-1 The simplified Boolean expression for the O/P Y is,



$Y = \overline{\overline{B\overline{A}} + \overline{B+C}}$
$= B\overline{A}\,(B+C)$
$= B\overline{A}\,(B+\overline{C})$
$= B\overline{A} + B\overline{A}\,\overline{C}$
$= B\overline{A}\,(1+\overline{C})$
$= \overline{A}B.$

gate - 2010

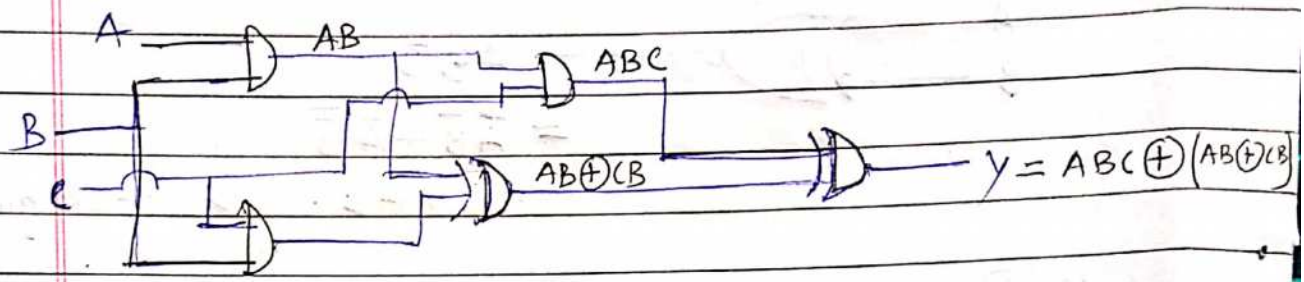
Q-2 Match the logic gates in column 'A' with their equivelents in column B.

Column A	Column B
P.  $\overline{A+B}$ (NOR)	1.  $A \oplus \overline{B} = \overline{A}\,\overline{B} + AB$ (EXNOR)
Q.  $\overline{AB}$ (NAND)	2.  $\overline{A} + \overline{B} = \overline{AB}$ (NAND)
R.  $A \oplus B$ (EXOR)	3.  $\overline{A} \odot B = A\overline{B} + \overline{A}B$ (EXOR)
S.  $A \odot B$ (EXNOR)	4.  $\overline{A}\,\overline{B} = \overline{A+B} =$ (NOR)

P → 4, Q → 2, R → 3, S → 1

gate - 2016

Q-3 The Output of the combinational circuit given below is:



(a) $A+B+C$ (b) $A(B+C)$

(c) ✓ $B(C+A)$ (d) $C(A+B)$

→

$Y = ABC \oplus (AB \oplus CB)$

$= ABC \oplus (\overline{AB}\cdot CB + AB\cdot\overline{CB})$

$= ABC \oplus [(\bar{A}+\bar{B})CB + AB(\bar{C}+\bar{B})]$

$= ABC \oplus (\bar{A}CB + AB\bar{C})$

$= \overline{ABC}(\bar{A}CB + AB\bar{C}) + ABC(\overline{\bar{A}CB + AB\bar{C}})$

$= (\bar{A}+\bar{B}+\bar{C})(\bar{A}CB + AB\bar{C}) + ABC\ (A+\bar{C}+\bar{B})\cdot(\bar{A}+\bar{B}+C)$

$= \bar{A}CB + AB\bar{C} + (A+\bar{C}+\bar{B})\ ABC$

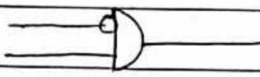
$= \bar{A}BC + AB\bar{C} + ABC$

$= BC + AB\bar{C}$

$= B(C+A\bar{C}) = B(C+A)$

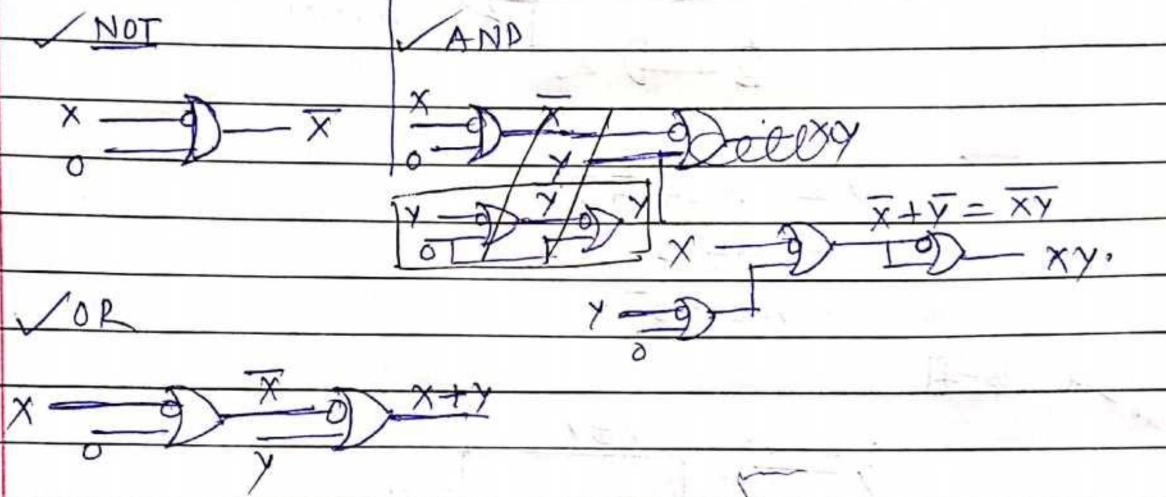
gate-2015

Q-4 Check given gate are universal or not –



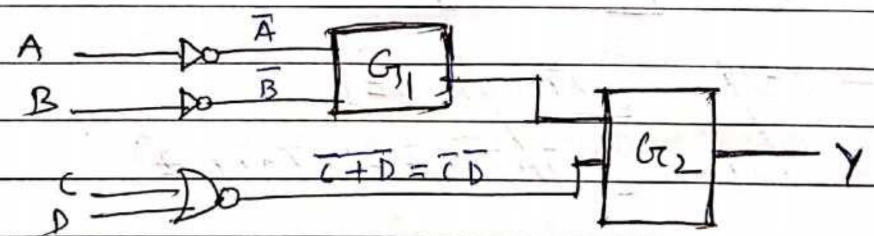
F = $\overline{X}+Y$

→ If a gate are universal, then we can Implement NOT, AND, OR, NAND, NOR.



so, given gate are universal gate.

gate-15 **Q-5** In the figer shown, the O/P Y is required to be $Y = AB + \overline{C}\,\overline{D}$. The gate G_1 and G_2 must be –

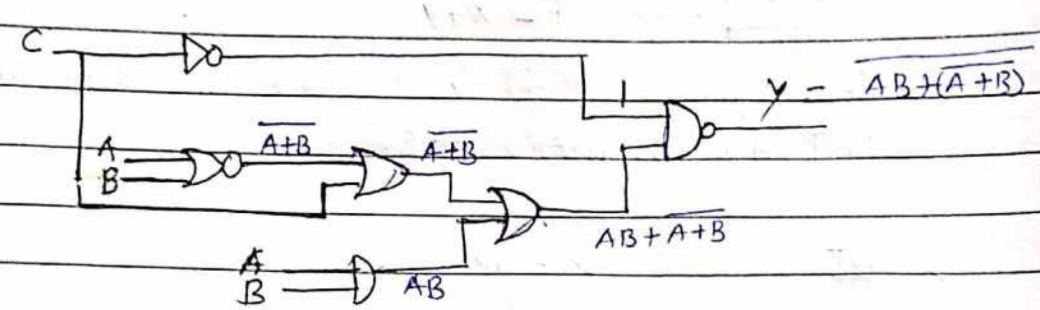


✓(a) NOR, OR (b) OR, NAND (c) NAND, OR (d) AND, NAND

→

$\overline{\overline{A}+\overline{B}} = \overline{\overline{A}}\cdot\overline{\overline{B}}$

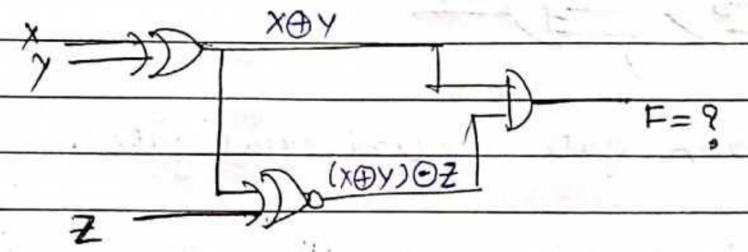
Gate-14 **Q-6** If $C=0$, $y=?$



$\rightarrow$

$$Y = \overline{AB + \overline{A}\,\overline{B}}$$
$$= A \oplus B$$
$$= A\overline{B} + B\overline{A}.$$

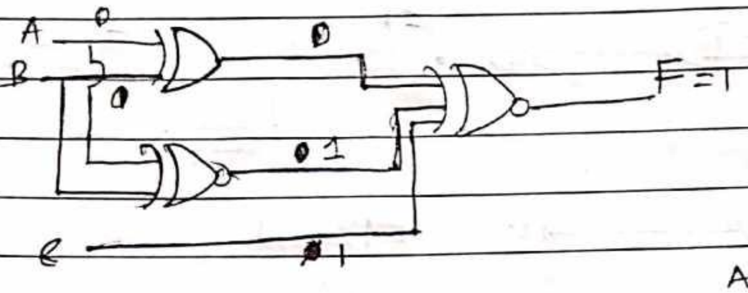
Gate-14 **Q-7**



$\rightarrow$

$$F = (X\oplus Y)\,[(X\oplus Y)\odot Z]$$
$$= (X\oplus Y)\,[(X\overline{Y} + \overline{X}Y)\odot Z]$$
$$= (X\oplus Y)\,[\overline{(X\overline{Y}+\overline{X}Y)}\,\overline{Z} + (X\overline{Y}+\overline{X}Y)\,Z]$$
$$= (X\oplus Y)\,[(X\oplus Y)\,Z + \overline{(X\oplus Y)}\cdot\overline{Z}]$$
$$= (X\oplus Y)\,Z + 0$$
$$= (\overline{X}Y + X\overline{Y})\,Z$$
$$= \overline{X}YZ + X\overline{Y}Z$$

Gate-10 **Q-8** for the output F to be '1' in the logic circuit shown, the i/p combination should be



A	B	C	F
0	0	0	0
1	1	0	0

× (a) A=1, B=1, C=0

0 11 = 0=1

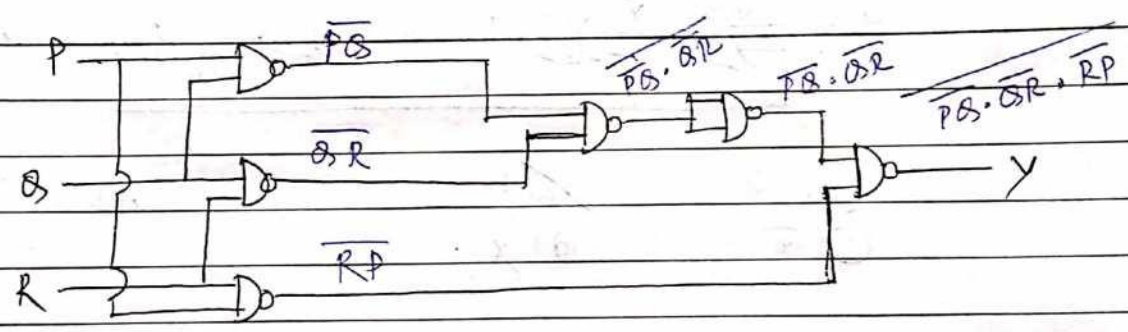
× (b) A=1, B=0, C=0

× (c) A=0, B=1, C=0

✓ (d) A=0, B=0, C=1.

010=1
100=1=0

G-11 **Q-9** The o/p y in the circuit below is always '1' when,



× (a) two or more of the i/ps P, Q, R are '0'.

✓ (b) two or more " " " are '1'.

(c) any odd no. of the i/ps P, Q, R is '0'.

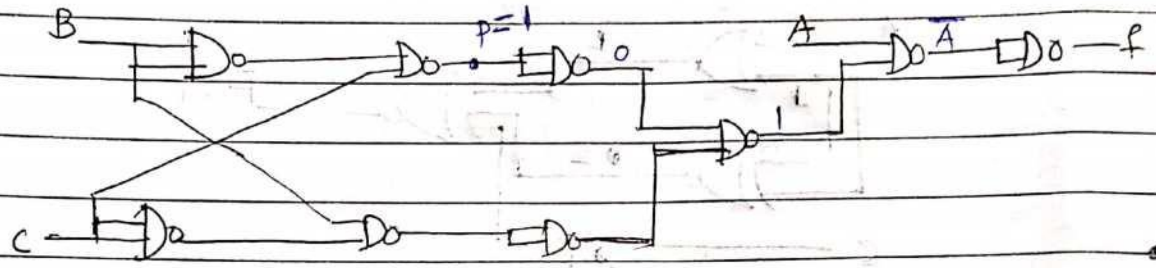
✓ (d) " " " " " is '1'.

→

$y = \overline{\overline{PQ}\ \overline{QR}\ \overline{PR}}$ P, Q = 1

$\boxed{y = PQ + QR + PR}$

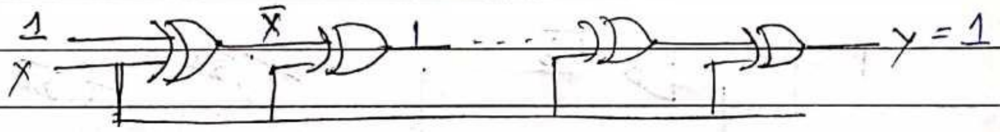
G-6 **[Q-10]** The point p in the following fig is stuck at 1, the o/p f will be –



(a) $\overline{AB\overline{C}}$ (b) $\overline{A}$

(c) $AB\overline{C}$ (d) A ✓

G-2 **[Q-11]** If the i/p to the dig ckt consisting of a cascade of 90- X-OR gates is X. Then the o/p Y is equal to –



(a) 0 (b) 1 ✓

(c) $\overline{X}$ (d) X.

$A \oplus B = \overline{A}B + A\overline{B}$
$= 0.X + 1\,\overline{X}$
$= \overline{X}$

$\overline{X} \oplus X = X.X + \overline{X}\,\overline{X}$
$= X + \overline{X}$
$= 1$